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From: Jacqueline J. Garner
Texas Instruments Incorporated
Facsimile: 972-917-4418
Phone: 214-532-9348

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Bailey et al.

Docket No.: TI-27935

Serial No.: 09/452,691

Examiner: Fenty, J.

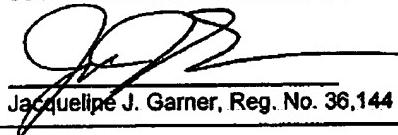
Filed: 12/02/99

Art Unit: 2815

For: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED
WITHIN A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER
METAL INTERCONNECT LAYER

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NAME OF INVENTOR(S):	
Bailey	
TITLE OF INVENTION:	
Method of Producing a Thin Film Resistor . . .	
TI FILE NO.:	DEPOSIT ACCT. NO.:
TI-27935	20-0668
FAXED: February 3, 2003	
DUE: 1/3/2003	
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Page 1 of 13

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COMPANY NAME:
TEXAS INSTRUMENTS INCORPORATED
PHONE NUMBER: (972) 917-
FAX NUMBER: (972) 917-4418

TO: J. Fenty
COMPANY NAME:
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From: Jacqueline J. Garner
Texas Instruments Incorporated
Facsimile: 972-617-4418
Phone: 214-532-0348

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:
Bailey et al. Docket No.: TI-27036

Serial No.: 09452,691 Examiner: Fasty, J.
Filed: 12/02/98 Art Unit: 2915

For: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED
WITHIN A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER
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NAME	Serial No.: 09452,691
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